

Docket No.: 60188-656

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of	:	Customer Number: 20277
	:	
Hiroshi TAKENAKA	:	Confirmation Number:
	:	
Serial No.: Divisional of Appl. No. 09/812,947	:	Group Art Unit:
	:	
Filed: January 06, 2004	:	Examiner:
	:	
For:		A SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE COMPRISING A PLURALITY OF SEMICONDUCTOR DEVICES FORMED ON A SUBSTRATE (AS AMENDED)

**INFORMATION DISCLOSURE STATEMENT**

Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

Serial No.: Divisional of Appl. No.

The references were cited by or submitted to the U.S. Patent and Trademark Office in parent application Serial No. 09/812,947, filed March 27, 2001, which is relied upon for an earlier filing date under 35 USC 120. Thus, copies of these references are not attached. 37 CFR 1.98(d).

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

MCDERMOTT, WILL & EMERY



Michael E. Fogarty  
Registration No. 36,139

600 13<sup>th</sup> Street, N.W.  
Washington, DC 20005-3096  
(202) 756-8000 MEF:prg  
Facsimile: (202) 756-8087  
**Date: January 6, 2004**

<b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b>  <b>(PTO-1449)</b>				ATTY. DOCKET NO. <b>60188-656</b>		SERIAL NO. <b>Divisional of Appl. No. 09/812,947</b>	
				APPLICANT <b>Hiroshi TAKENAKA</b>			
				FILING DATE <b>January 06, 2004</b>		GROUP	
<b>U.S. PATENT DOCUMENTS</b>							
EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
		US 5,729,469	03/1998	Kawakami			
		US 5,636,129	06/1997	Her			
		US 5,784,289	07/1998	Wang			
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		US					
<b>FOREIGN PATENT DOCUMENTS</b>							
EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes-Number + -Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation	
						Yes	No
		JP 63-258042 A	10/25/1988	NEC KYUSHU LTD.			
		JP 3-142857 A	06/18/1991	TOSHIBA CORP.			
		JP 60-224243 A	11/08/1985	TOSHIBA CORP.			
		JP 2-21636 A	01/24/1990	MATSUSHITA ELECTRIC IND. CO., LTD.			
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		JP 63-257226 A	10/25/1988	NEC CORP.			
		JP 62-156816 A	07/11/1987	TOSHIBA CORP.			
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		JP 10-223508 A	08/21/1998	NEC CORP.			
		JP 11-121723 A	04/30/1999	TOSHIBA MICROELECTRONICS CORP. & TOSHIBA CORP.			
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.					
EXAMINER				DATE CONSIDERED			

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.